Flash: An efficient and portable Web Server

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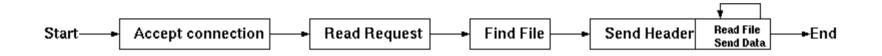
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Overview

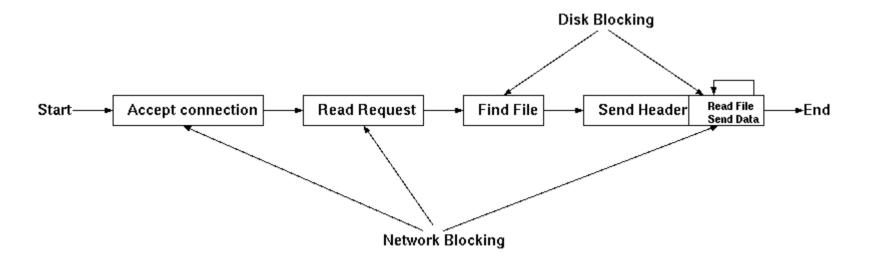
- Motivation
- Server Architectures
- Design comparison
- Flash Implementation
- Performance Evaluation
- Conclusion

- Goals:
 - \rightarrow High throughput,
 - \rightarrow Good portability,
 - \rightarrow Wide range of workloads.

• Basic processing steps performed by a Web Server



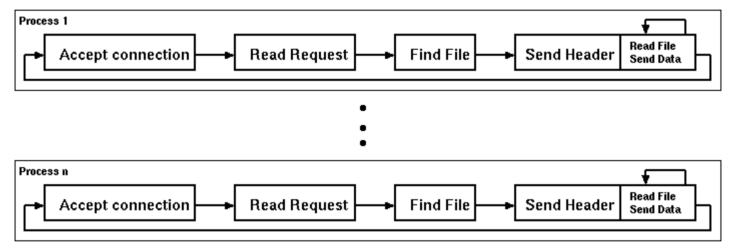
• Problem: Blocking steps



- Solution:
 - \rightarrow concurrency architectures
 - overlap CPU processing with disk accesses and network communication.
 - \rightarrow caching
- Architecture strategy, used to achieve the interleaving.

• Multi-Process architecture

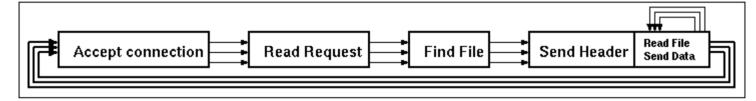




- + many HTTP requests may be served concurrently,
- + relies on OS.
- many processes.

• Multi-Threaded architecture

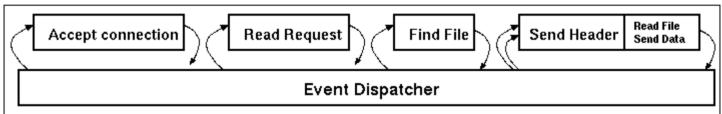




- + shared global variables.
- many threads,
- requires kernel thread support,
- requires synchronization.



Single-process event-driven architecture (SPED)

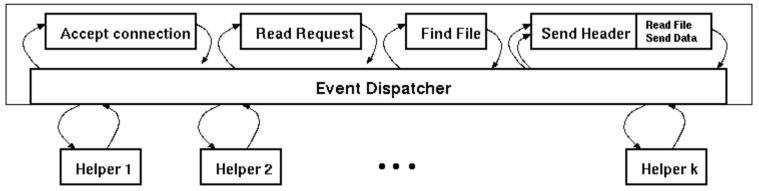


- + single address space,
- + no context switching required,
- + no synchronization required.
- in practice, disk reads still block.

- Desired:
 - \rightarrow Shared (single) address space,
 - \rightarrow Good disk behavior,
 - \rightarrow No synchronization.

• New architecture:

Asynchronous/Asymmetric Multi-Process Event-Driven architecture (AMPED)



Design comparison 1

• Performance characteristics

	MP	MT	SPED	AMPED
Disk operations	Only the process that causes the disk activity is blocked	Only the thread that causes the disk activity is blocked	The main server process is blocked	Only the helper process that handles the disk activity is blocked
Memory consumption	High memory requirements	Single process memory requirements plus memory requirements for each thread employed	Single process memory requirements	Single process memory requirements plus additional memory for the helper processes
Disk utilization	One disk request per process	One disk request per thread	One disk at a time	One disk request per helper

Design comparison 2

Cost/Benefits of optimizations

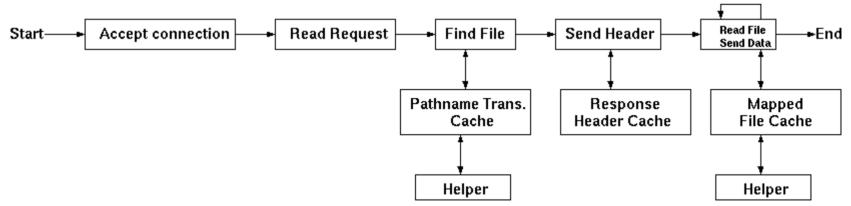
	MP	MT	SPED	AMPED
Information gathering	Requires some form of IPC in order to consolidate data	Requires synchronization on global variables	Simple information gathering since all requests are processed in a centralized fashion	Simple information gathering since all requests are processed in a centralized fashion
Application level caching	Each process may have its own cache	Single cache with synchronization	Single cache without synchronization	Single cache without synchronization
Long-lived connections	Overhead of an extra process for each connection	Overhead of an extra thread for each connection	Overhead of a file descriptor, application-level information and some kernel state for the connection	Overhead of a file descriptor, application-level information and some kernel state for the connection

Flash Implementation 1

- High performance implementation of the AMPED architecture.
- Various optimizations.

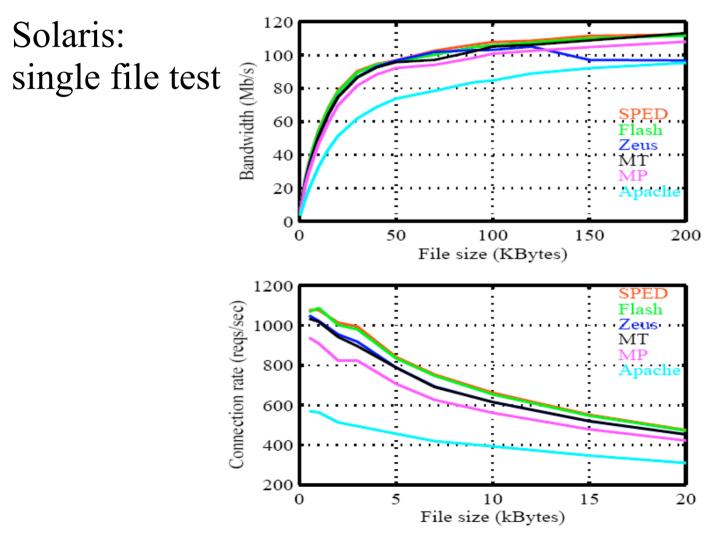
Flash Implementation 2

- Three types of caching:
 - \rightarrow pathname translation caching,
 - \rightarrow response header caching,
 - \rightarrow mapped files caching.

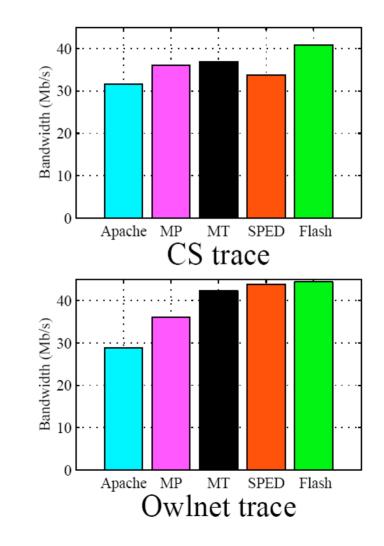


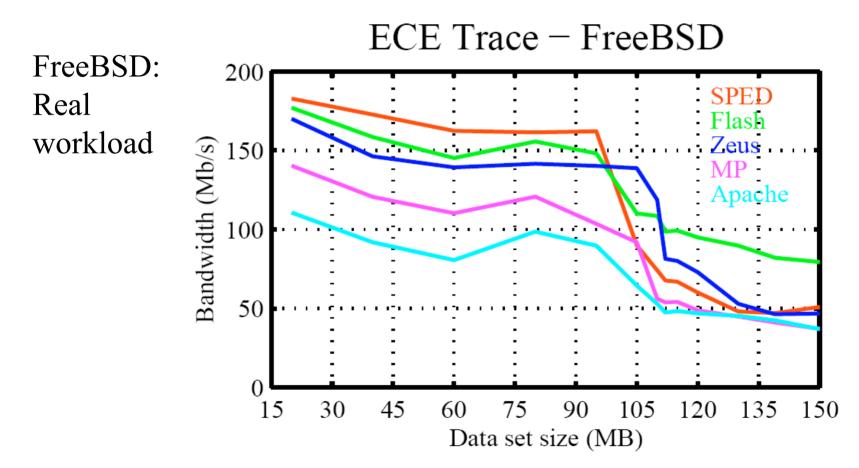
- Test environment
 - → Server Hardware:
 - 333 MHz Pentium II
 - 128 MB memory
 - Five 100 Mbit/s Ethernet interfaces
 - → Operating Systems:
 - FreeBSD 2.2.6
 - Solaris 2.6

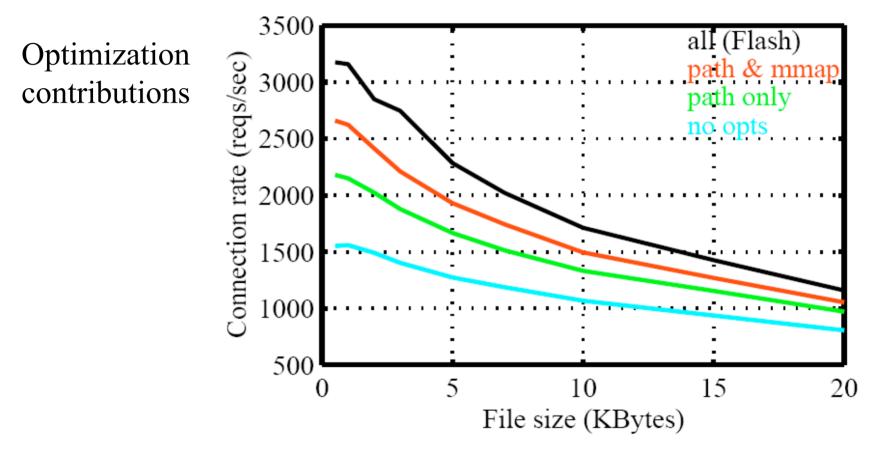
- Test environment
 - → Server Software:
 - Apache 1.3.1 MP
 - Zeus 1.30 SPED
 - Flash AMPED
 - Flash-<mark>SPED</mark>
 - Flash-MP
 - Flash-MT



Solaris: Rice Server Traces







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Conclusion

- Goals achieved?
- Good performance on real workloads
 → up to 30% faster than Zeus,
 → up to 50% faster than Apache.

Thank you for your attention!