

# Quantitatively Relaxed Concurrent Data Structures

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# Semantics of concurrent data structures

- Sequential specification – set of legal sequences
- Correctness condition – linearizability



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Stack – legal sequence

`push(a)push(b)pop(b)`

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Stack – concurrent history

`begin-push(a)begin-push(b) end-push(a) end-push(b)begin-pop(b)end-pop(b)`



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linearizable  
wrt seq.spec.

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we relax this

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linearizable wrt seq.spec.

Stack - concurrent history

`begin-push(a)begin-push(b) end-push(a) end-push(b)begin-pop(b)end-pop(b)`

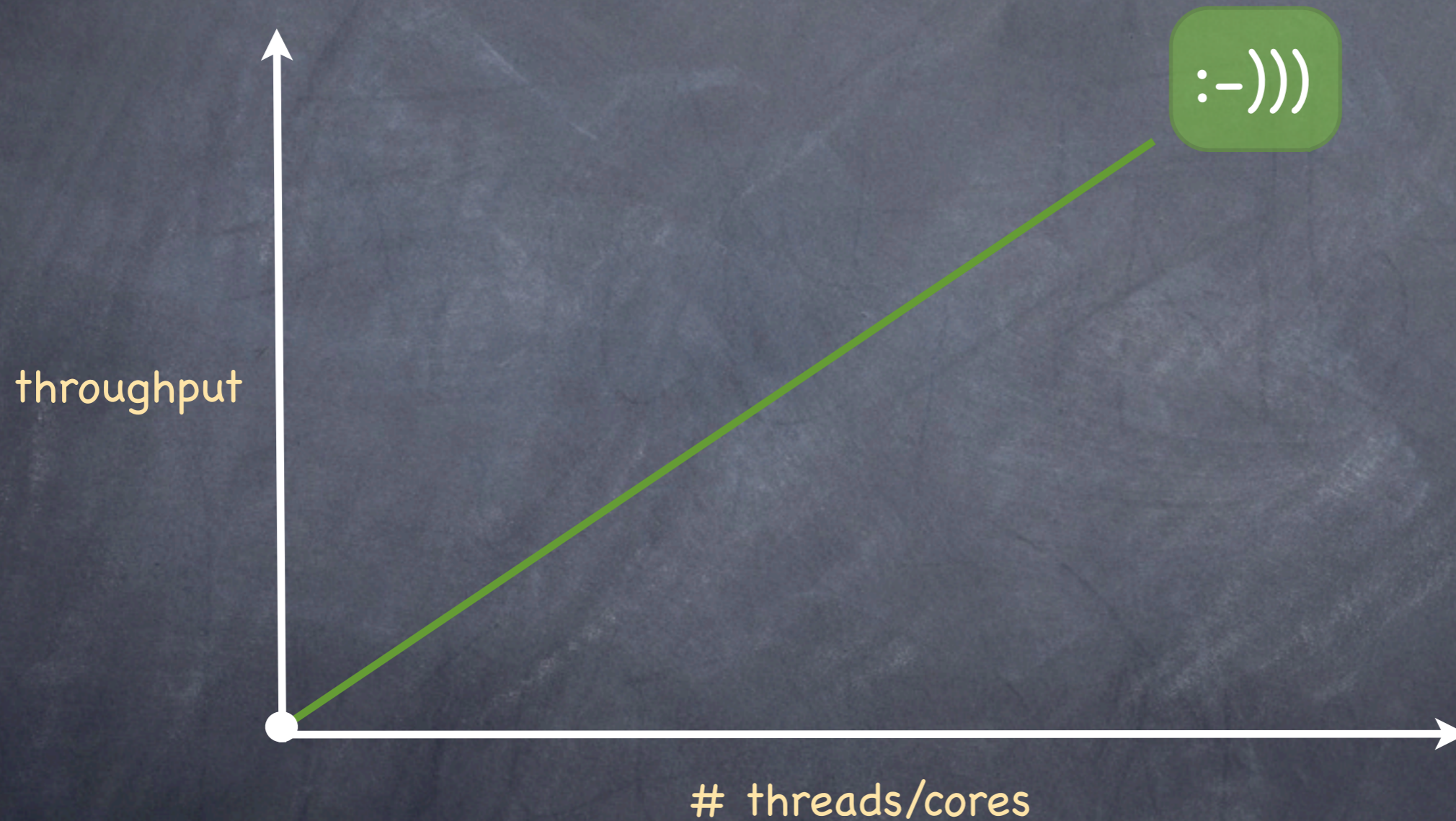


# Performance and scalability



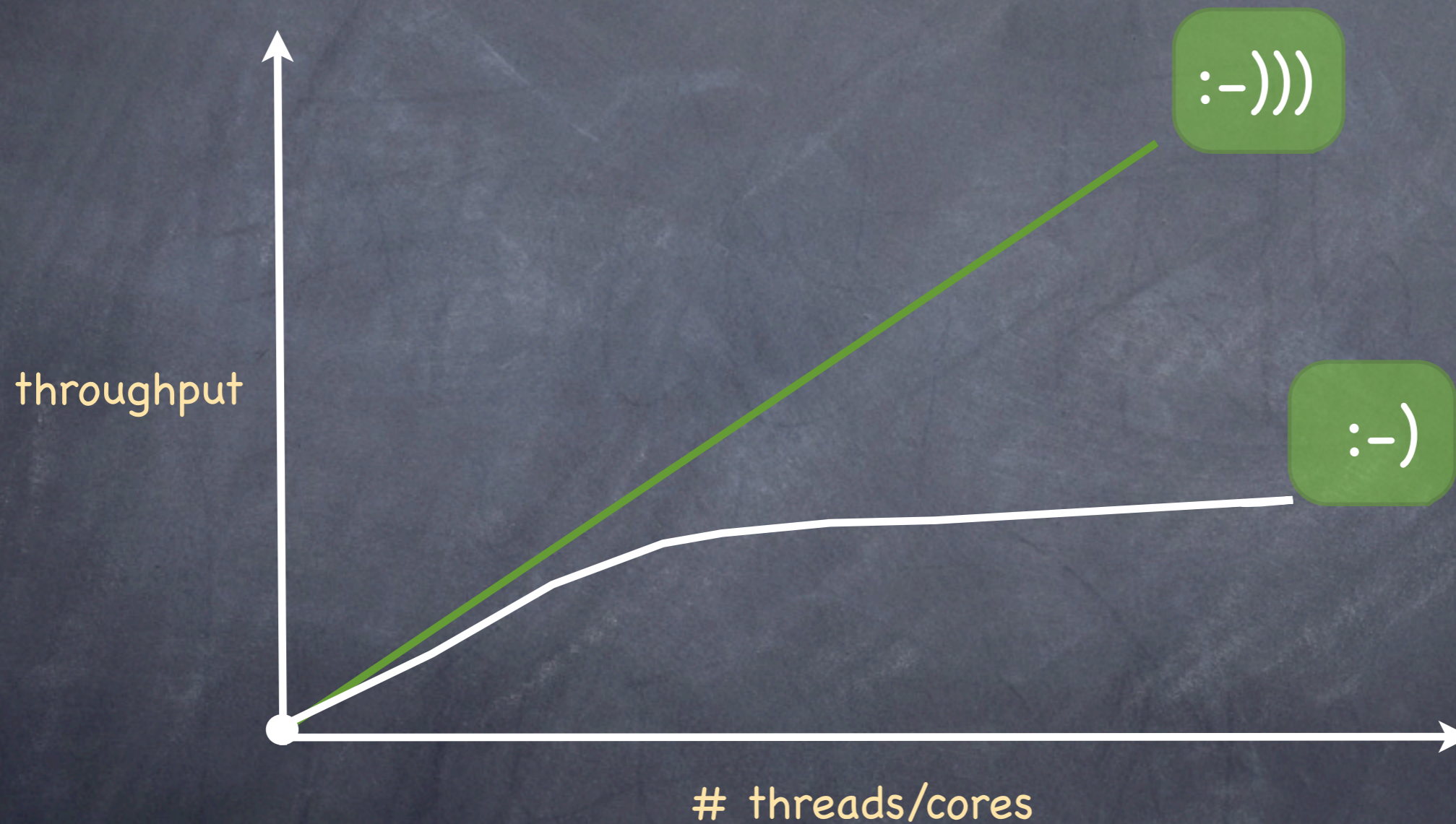


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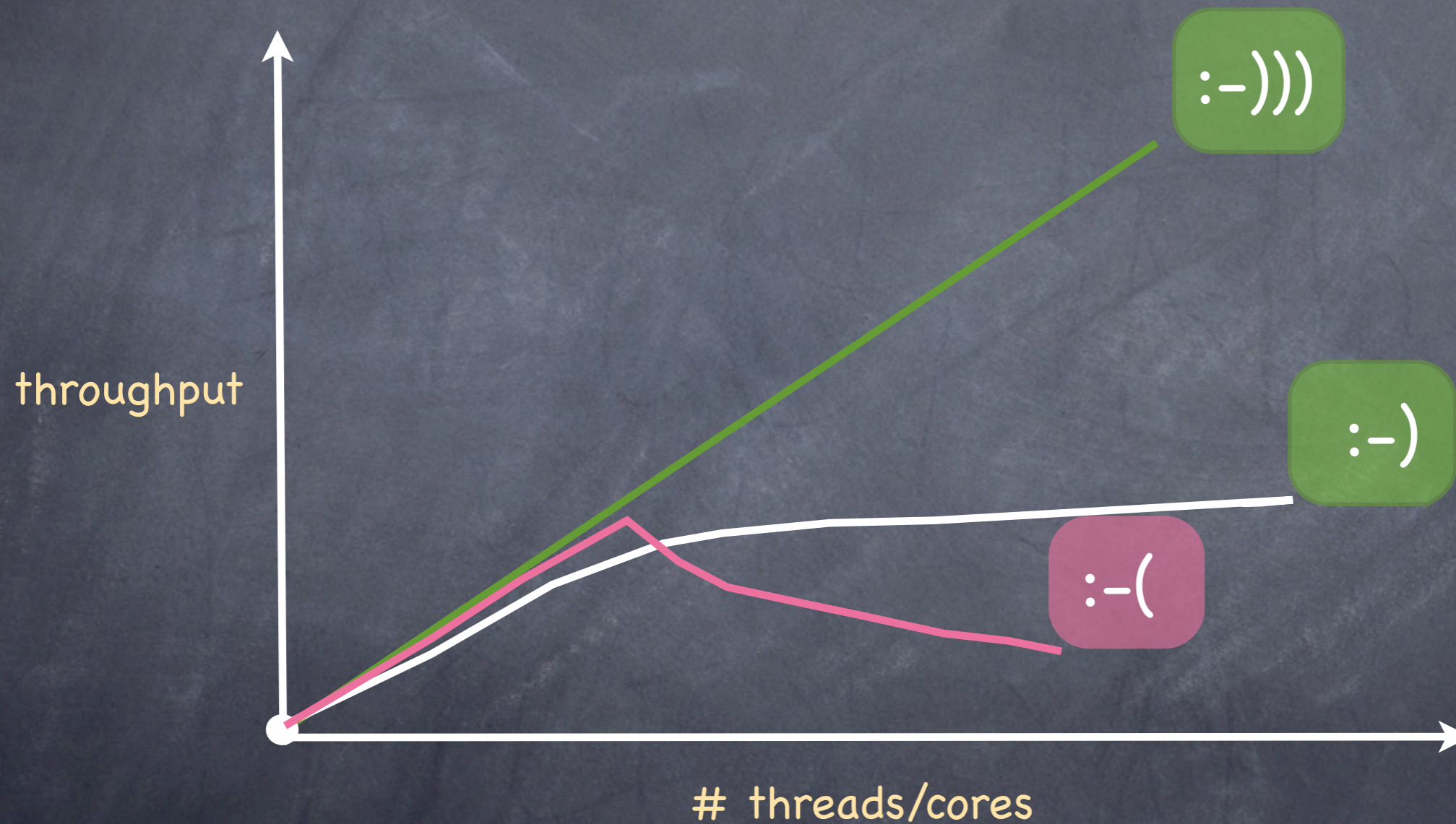


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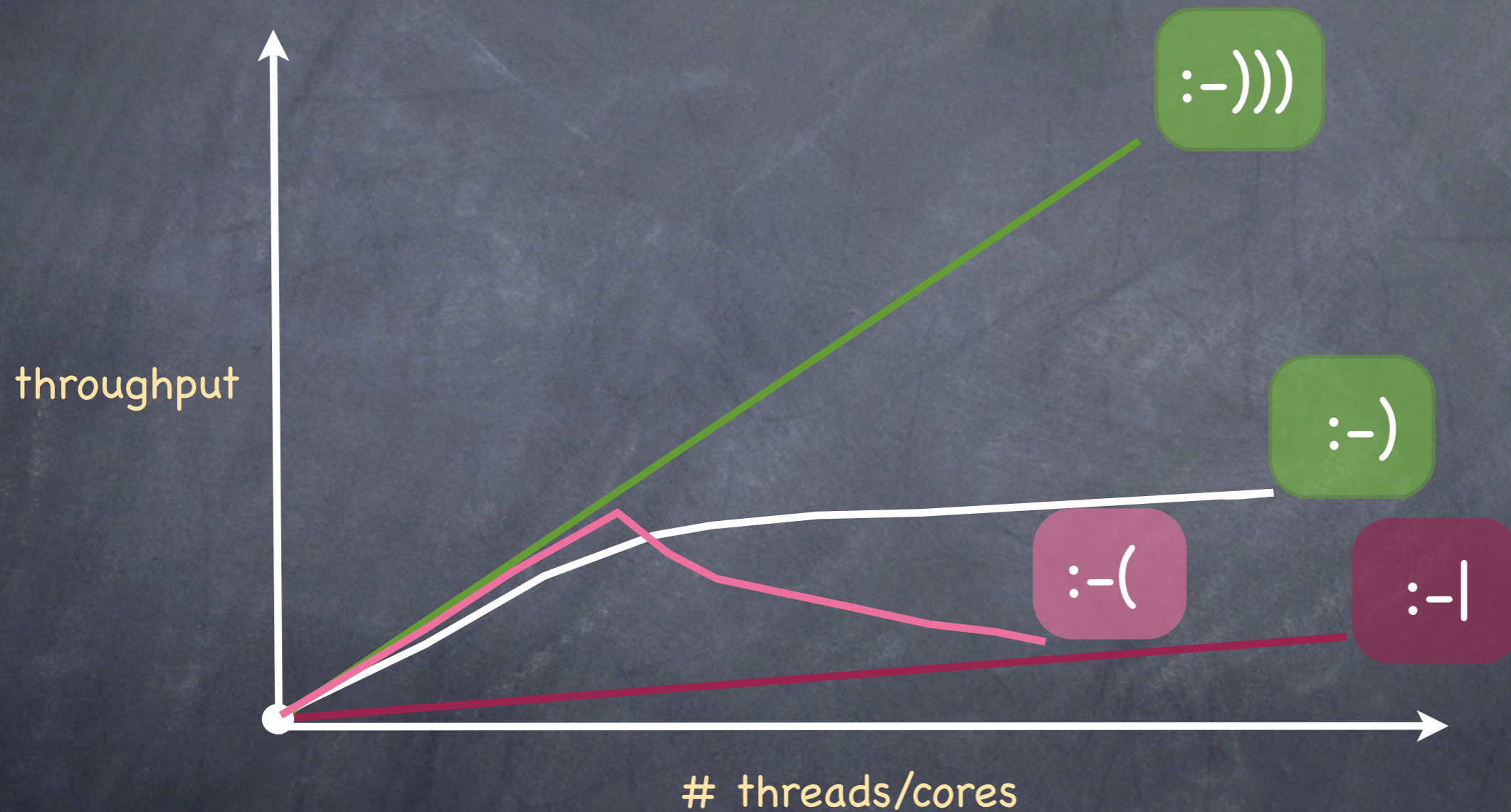


# Performance and scalability





# Performance and scalability





# The goal

- Trading **correctness** for **performance**
- In a controlled way with **quantitative bounds**



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- Trading **correctness** for **performance**
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measure the error from  
correct behavior



# The goal

Stack – incorrect behavior

```
push(a)push(b)push(c)pop(a)pop(b)
```

- Trading **correctness** for **performance**
- In a controlled way with **quantitative bounds**

correct in a relaxed stack  
... 2-relaxed? 3-relaxed?

measure the error from  
correct behavior



# Stack example

`push(a)push(b)push(c)pop(a)pop(b)`

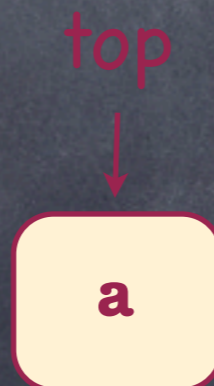
state evolution



# Stack example

**push(a)** push(b) push(c) pop(a) pop(b)

state evolution

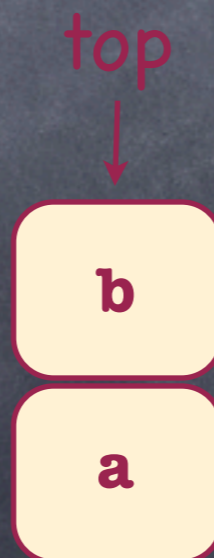




# Stack example

`push(a)` **`push(b)`** `push(c)` `pop(a)` `pop(b)`

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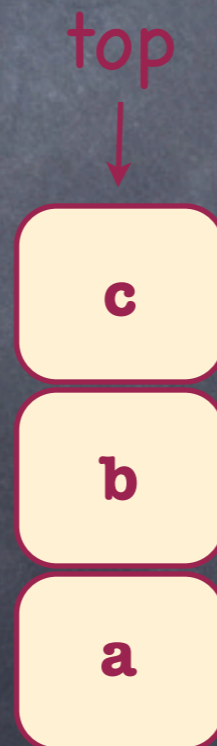




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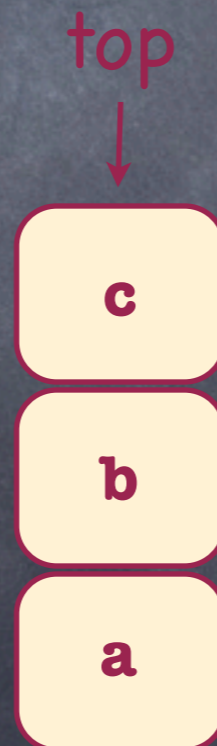


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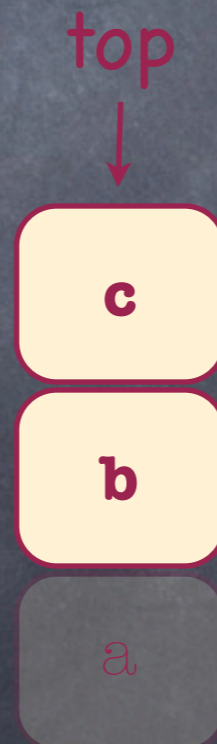


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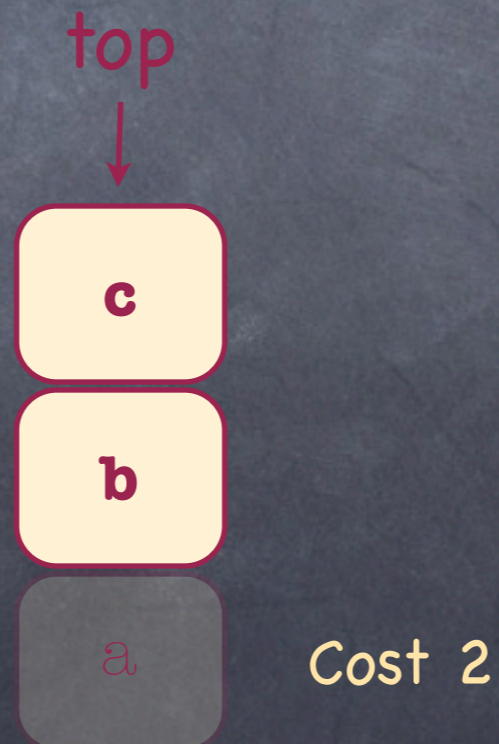
How much does this error cost?



# Stack example

`push(a) push(b) push(c) pop(a) pop(b)`

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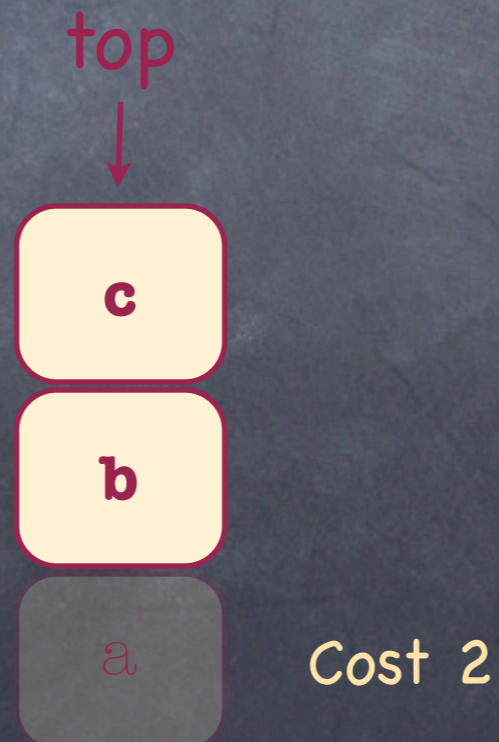


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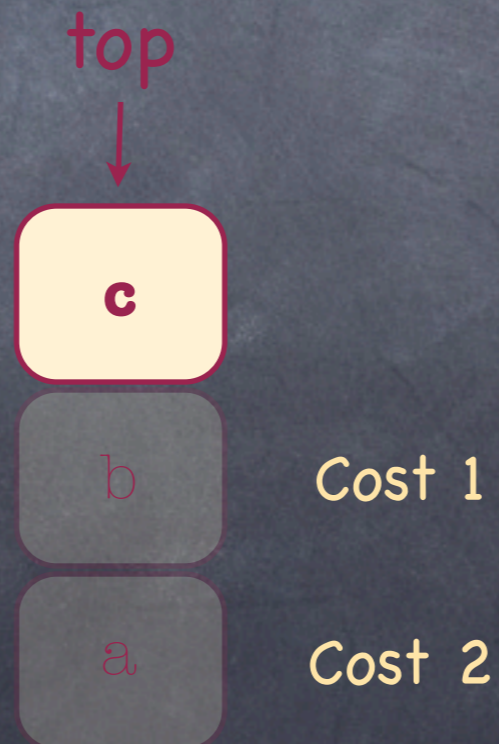




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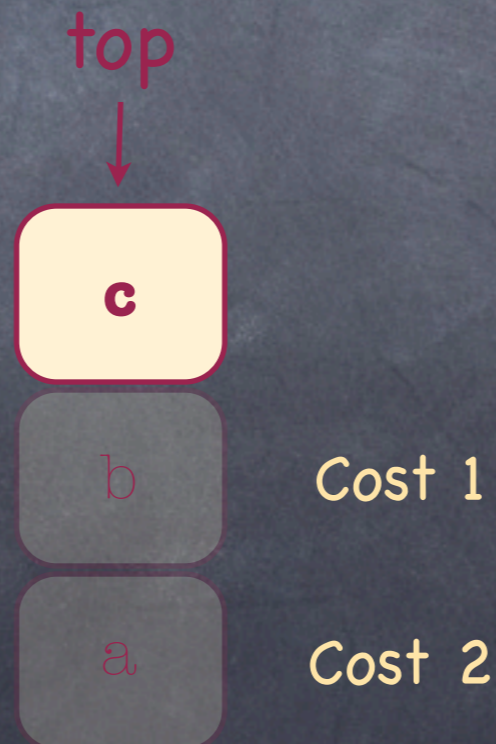




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Total  
cost?



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# Why relax?

- It is theoretically interesting
- Provides potential for **better performing** concurrent implementations

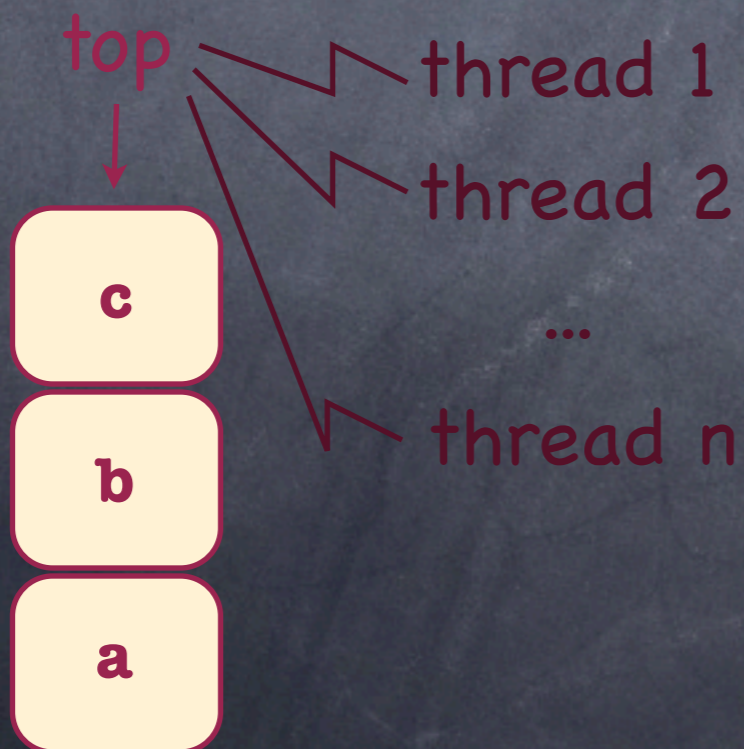
...



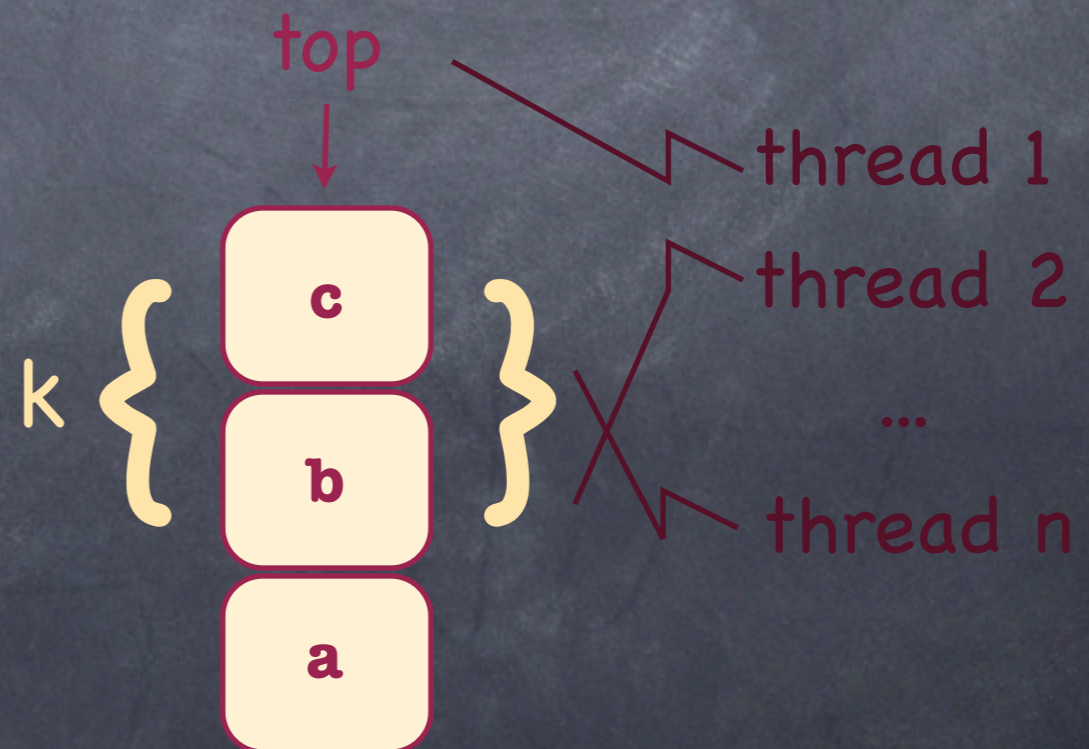
# Why relax?

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Stack



k-Relaxed stack





# What we have

• Framework

for semantic  
relaxations

• Generic examples

out-of-order /  
stuttering

• Concrete relaxation examples

stacks, queues,  
priority queues,.. /  
CAS, shared counter

• Efficient concurrent implementations

of relaxation  
instances



# Enough introduction

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# The big picture

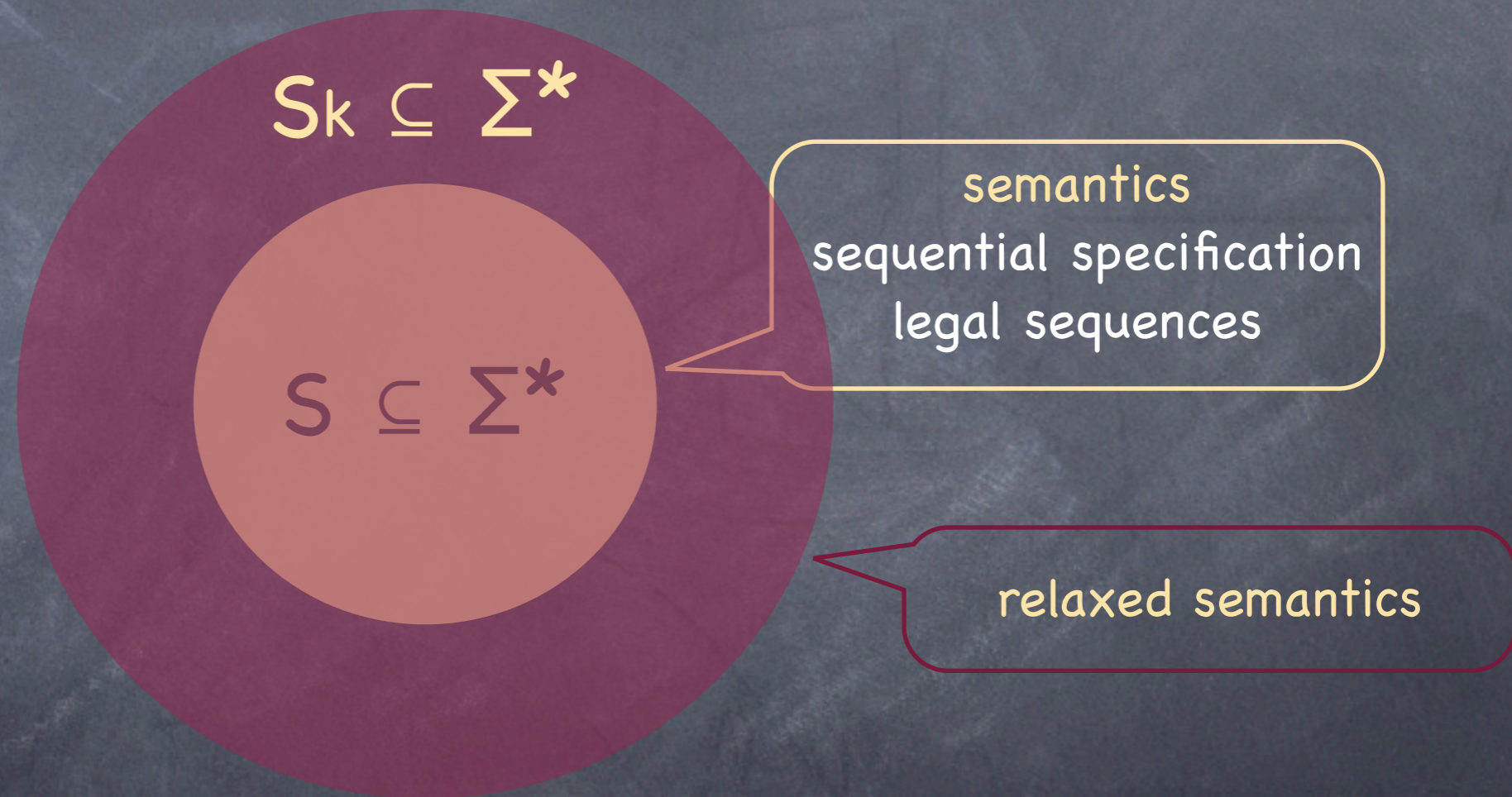
$$S \subseteq \Sigma^*$$

semantics  
sequential specification  
legal sequences

$\Sigma$  - methods with arguments



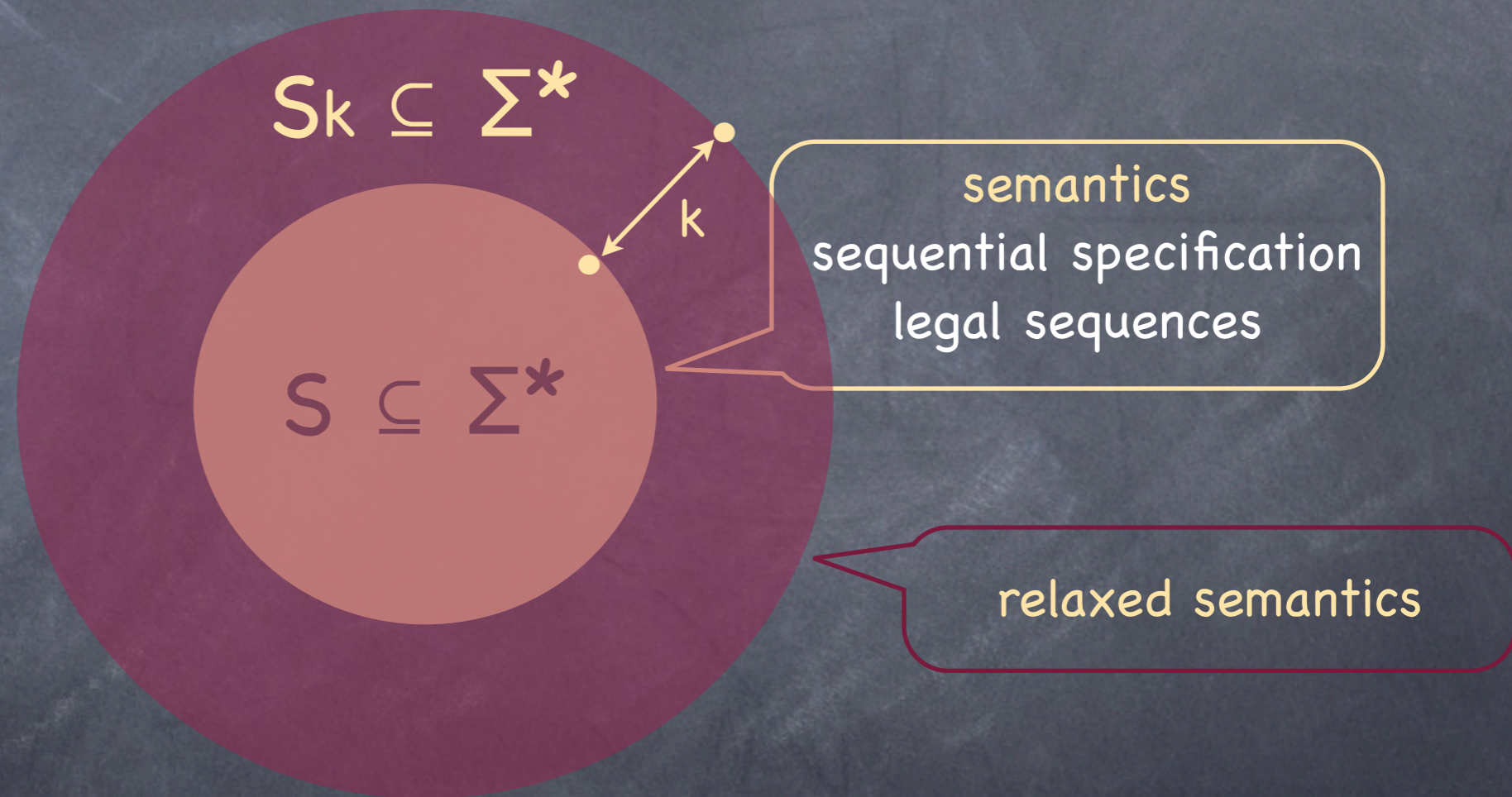
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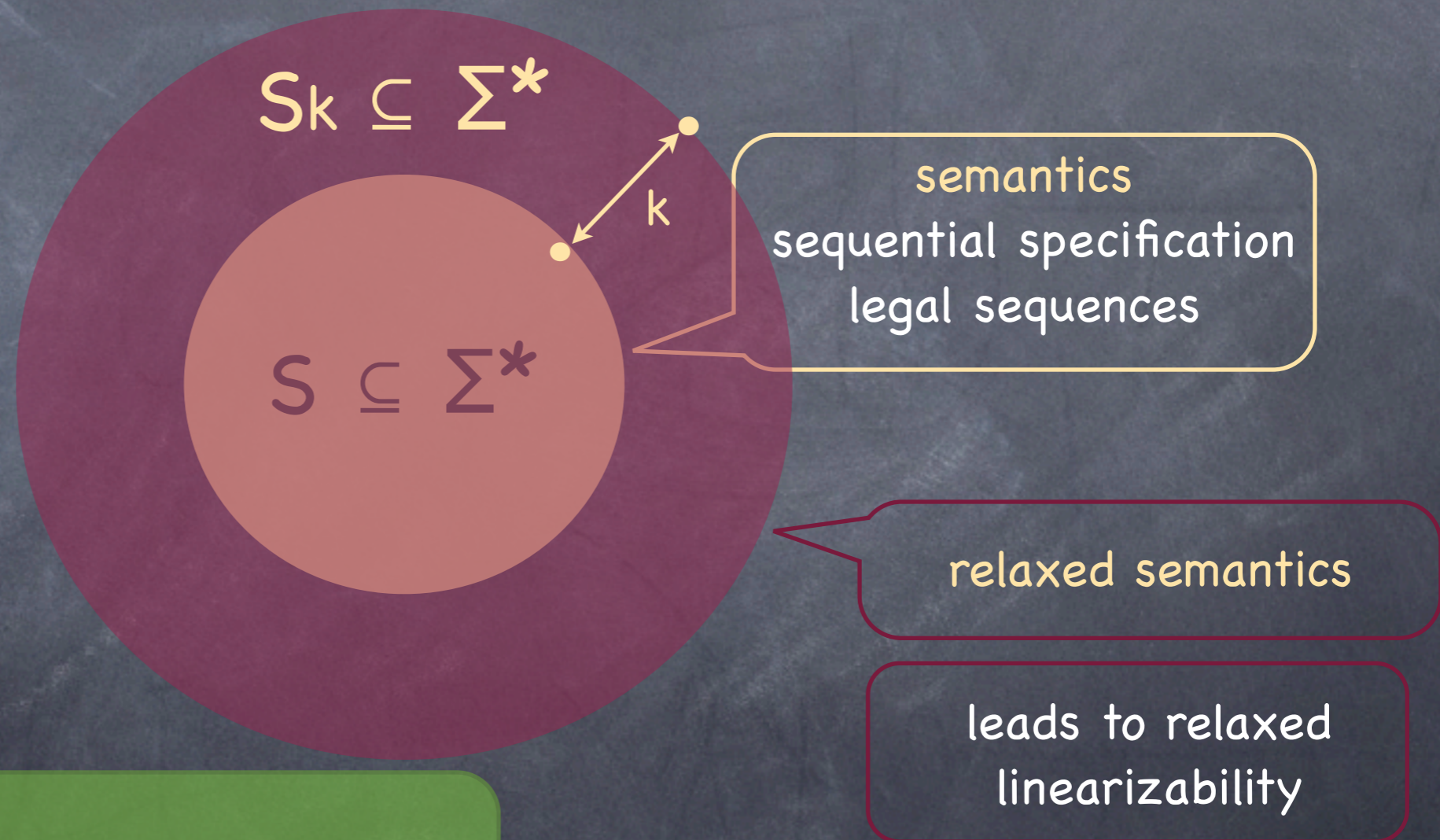
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# Theoretical challenge

There are natural concrete relaxations...

Stack

Each **pop** pops one of the  $k$ -youngest elements

Each **push** pushes .....



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$k$ -out-of-order  
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makes sense also for queues,  
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Each **pop** pops one of the  $k$ -youngest elements

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$k$ -out-of-order  
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makes sense also for queues,  
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How is it reflected by a distance between sequences?

one distance for all?



# Syntactic distances do not help

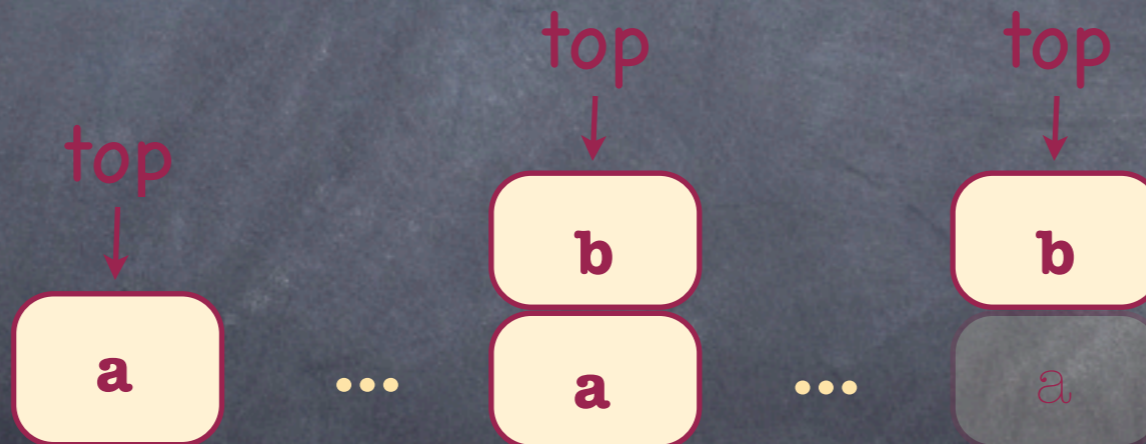
$\text{push}(a) [\text{push}(i)\text{pop}(i)]^n \text{push}(b) [\text{push}(j)\text{pop}(j)]^m \text{pop}(a)$



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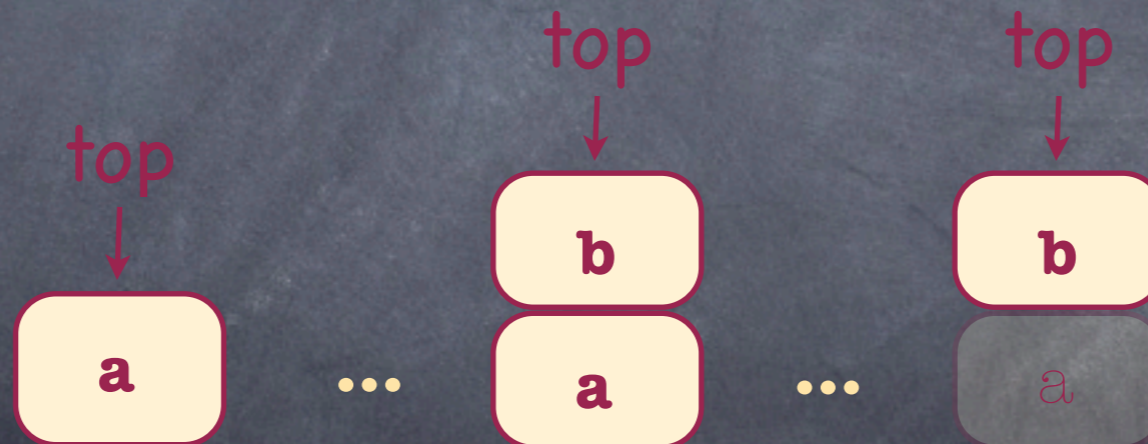




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its permutation distance is  $\min(n,m)$



# Semantic distances need a notion of state

- States are equivalence classes of sequences in  $S$
- Two sequences in  $S$  are equivalent if they have an indistinguishable future



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$$\mathbf{x} \equiv \mathbf{y} \iff \forall \mathbf{u} \in \Sigma^*. (\mathbf{xu} \in \mathbf{S} \iff \mathbf{yu} \in \mathbf{S})$$



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example: for stack

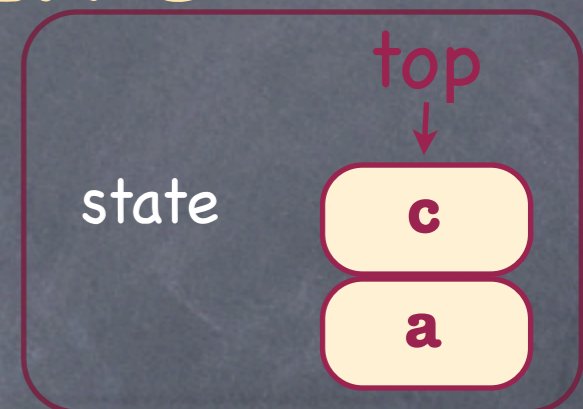
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# Semantics goes operational

- $S \subseteq \Sigma^*$  is the sequential specification

states

labels

initial state

- $LTS(S) = (S/\equiv, \Sigma, \rightarrow, [\varepsilon]_{\equiv})$  with

transition relation

$$[s]_{\equiv} \xrightarrow{m} [sm]_{\equiv} \iff sm \in S$$



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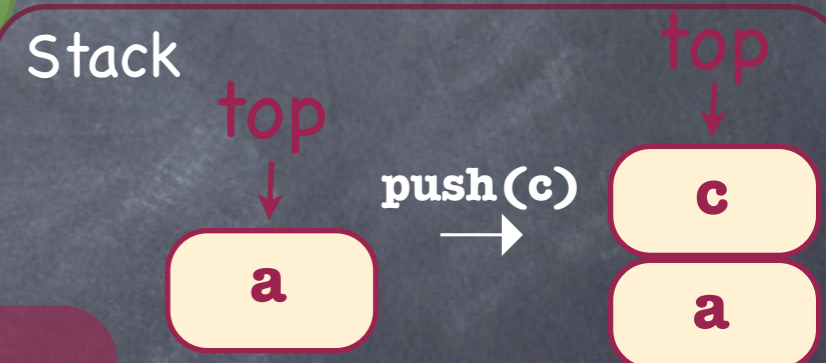
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# The framework

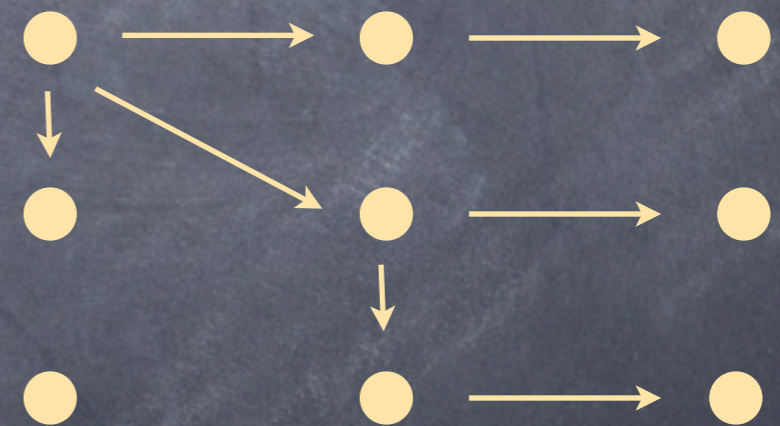
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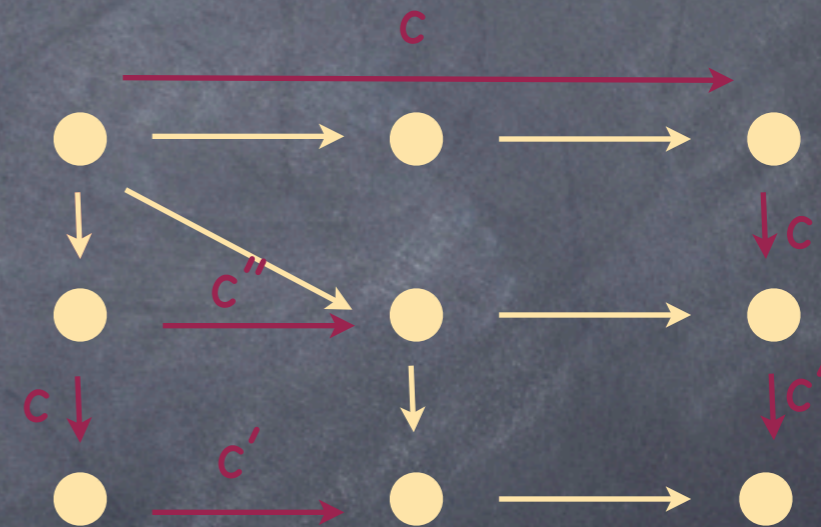
$\Sigma$  - singleton





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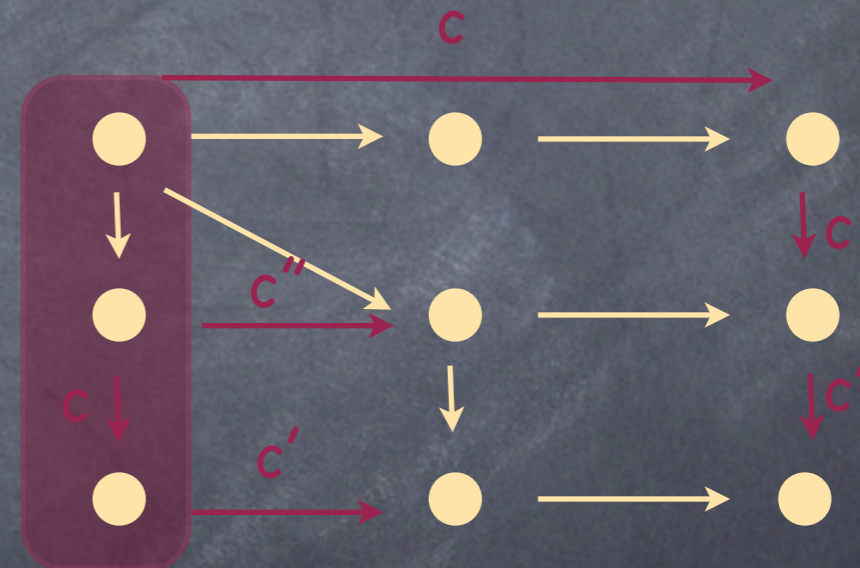






# The framework

- Start from LTS(S)
- Add transitions with transition costs
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distance - minimal cost on all paths  
labelled by the sequence



# For the user

- Pick your favorite data structure  $S$
- Add desired incorrect transitions and assign them transition costs
- Choose a path cost function

distance and relaxation follow



# For the user

The framework clears the head,  
direct concrete relaxations are also possible

- Pick your favorite **data structure S**
- **Add** desired **incorrect transitions** and assign them **transition costs**
- Choose a **path cost function**

distance and relaxation follow



# Stack example

`push(a) push(b) push(c) pop(a) pop(b)`

state evolution

Total  
cost





# Stack example

- Canonical representative of a state
- Add incorrect transitions with costs
- Possible path cost functions **max**, **sum**,...



# Stack example

Sequence of **push**'s with no matching **pop**

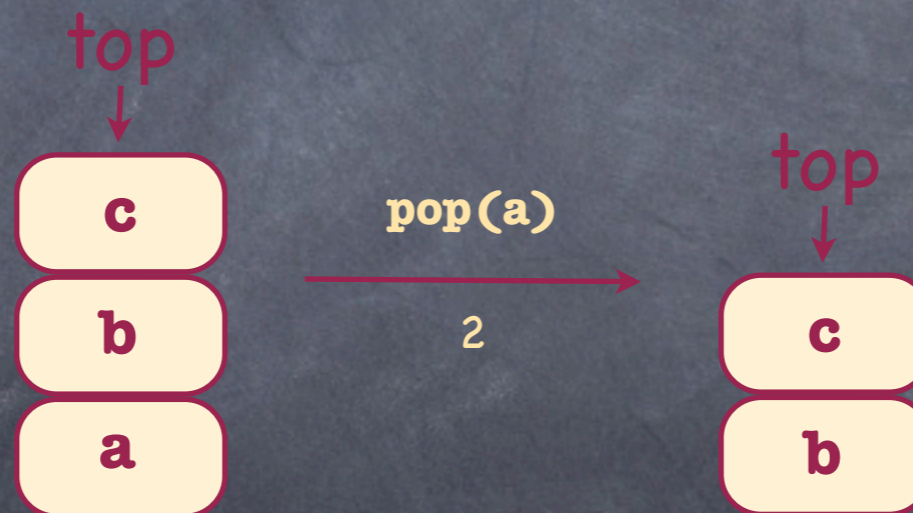
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It's more general...

---



# Generic out-of-order

$$\text{segment\_cost}(q \xrightarrow{m} q') = |\mathbf{v}|$$

transition cost

where  $\mathbf{v}$  is a sequence of minimal length s.t.

(1)  $[\mathbf{uvw}]_{\equiv} = q$ ,  $\mathbf{uvw}$  is minimal,  $\mathbf{uw}$  is minimal

(1.1) removing  $\mathbf{v}$  enables a transition

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goes with different path costs



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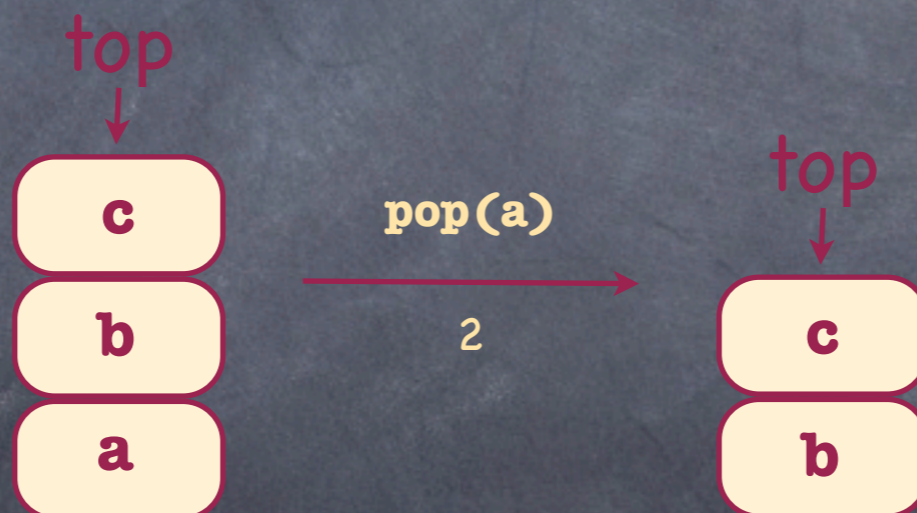
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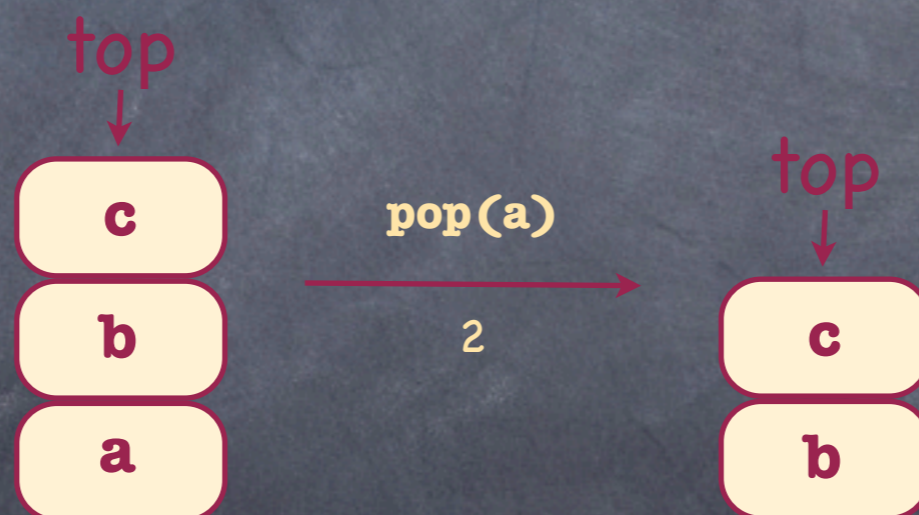
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also "shrinking window"  
restricted out-of-order



# Out-of-order queue

- Canonical representative of a state
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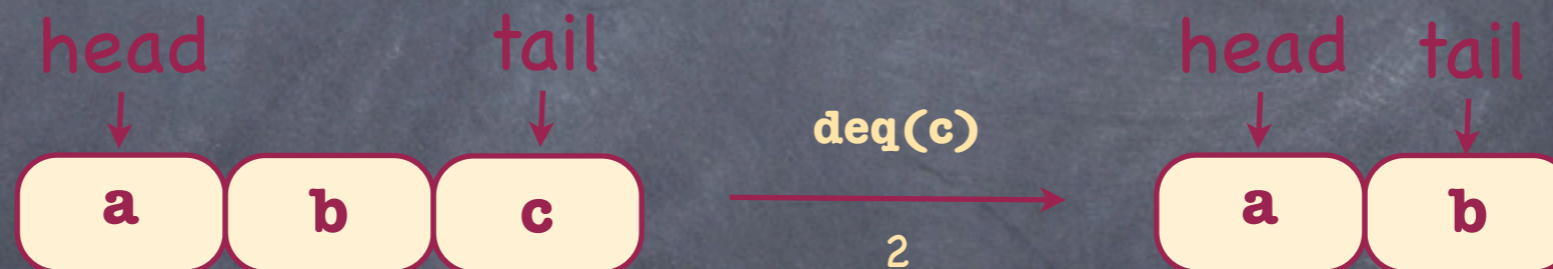
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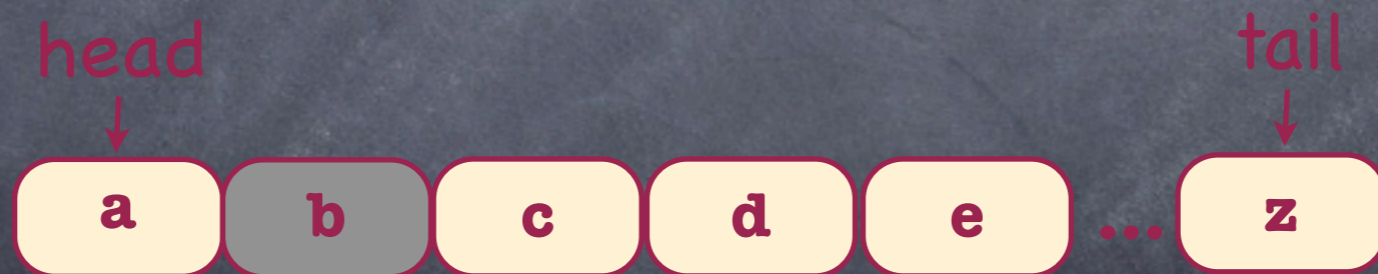
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# Out-of-order variants

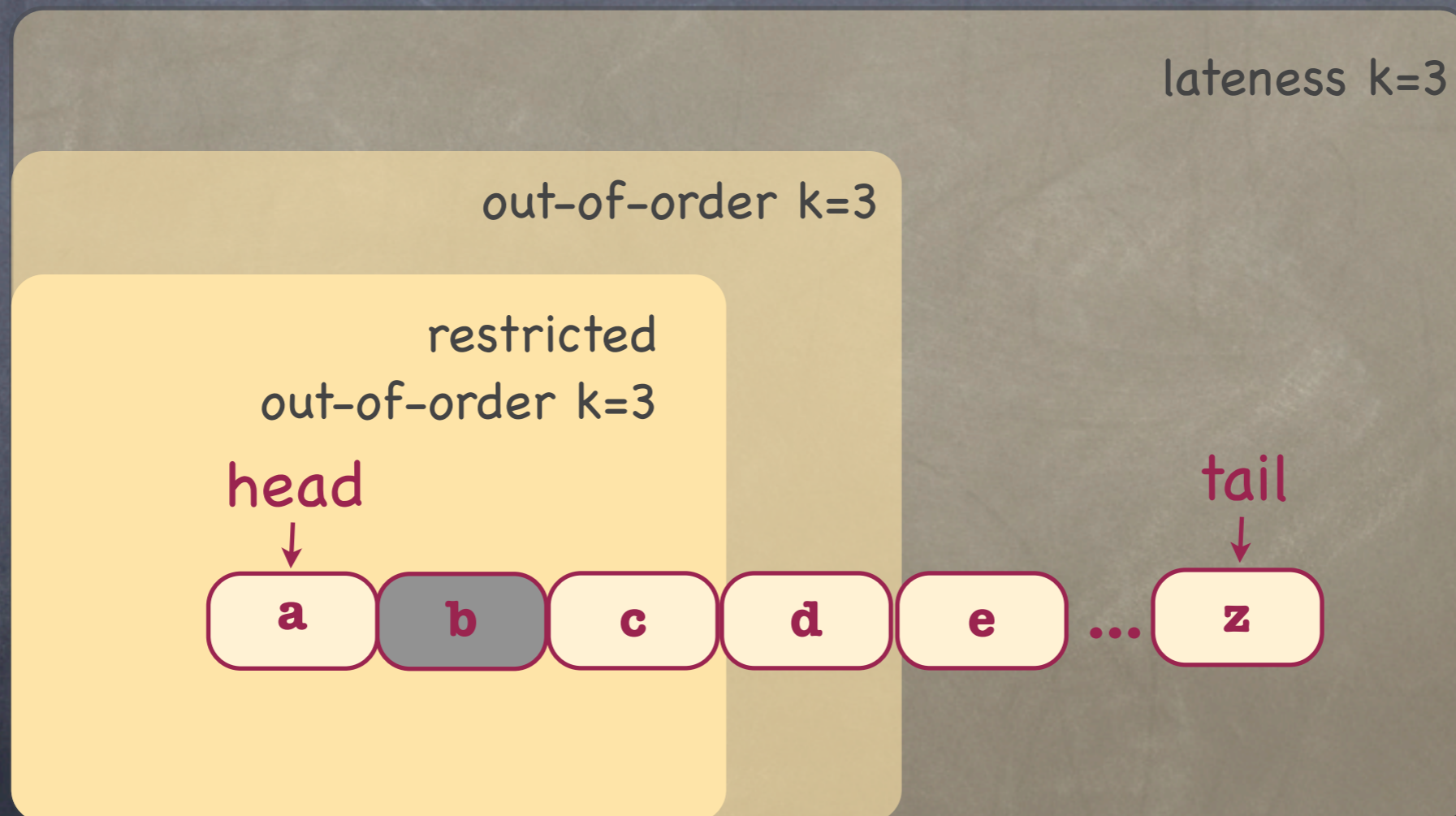
Queue





# Out-of-order variants

## Queue





How about  
implementations?  
Performance?

---



# Short-term history

- SCAL queues [KPRS'11]
- Quasi linearizability theory and implementations [AKY'10]
- Some straightforward implementations [HKPSS'12]
- Efficient lock-free segment queue [KLP'12]

(almost) all implement  
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# Short-term history

distributed, one  
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- performs very well
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# Lessons learned



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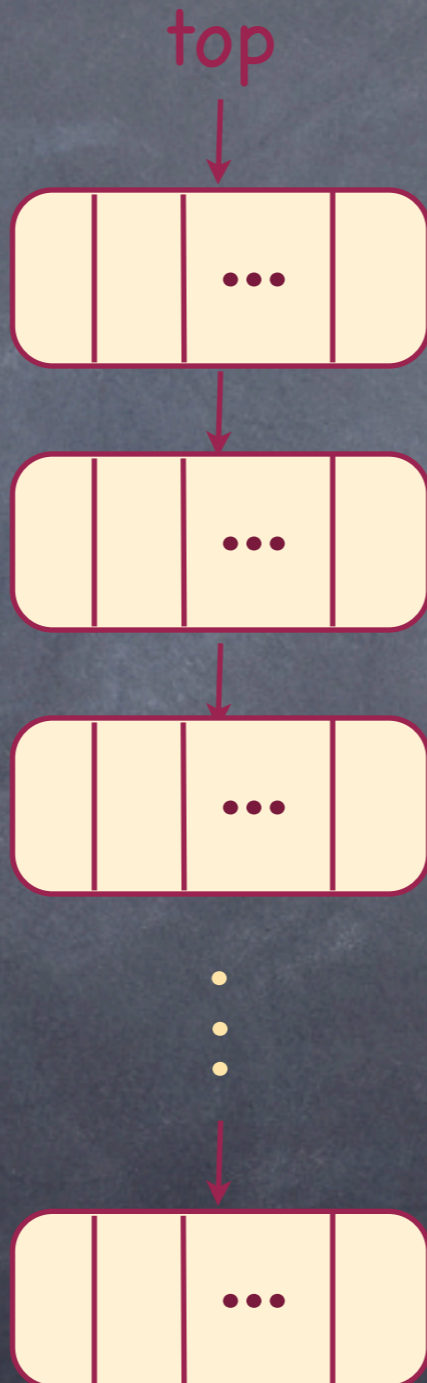
Well-performing implementations of relaxed specifications do exist!

Let's see them!



# Restricted-out-of-order k-Stack

lock-free = non-blocking

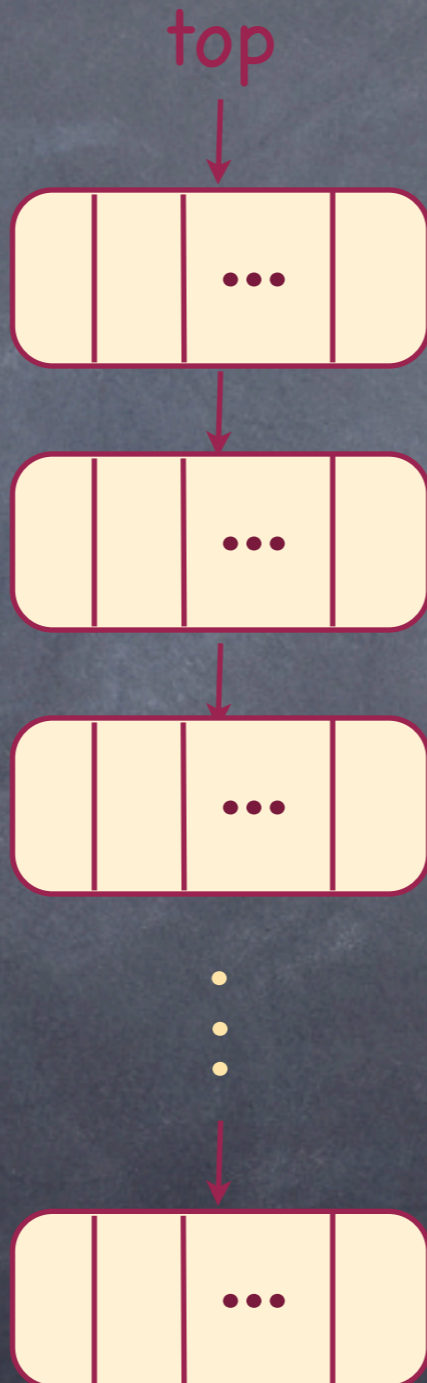


k-segment



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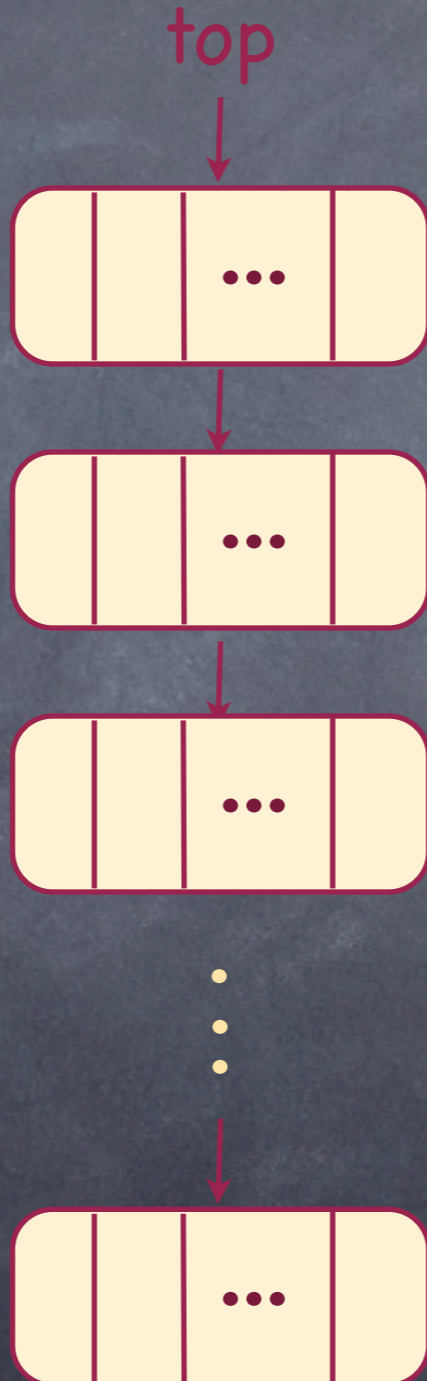
k-segment

```
1: loop:
2:   read consistent state
3:   try to add/remove an item (*)
4:   if successful:
5:     return
6:   else:
7:     try to repair the stack
8:     goto loop (retry)
```



# Restricted-out-of-order k-Stack

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k-segment

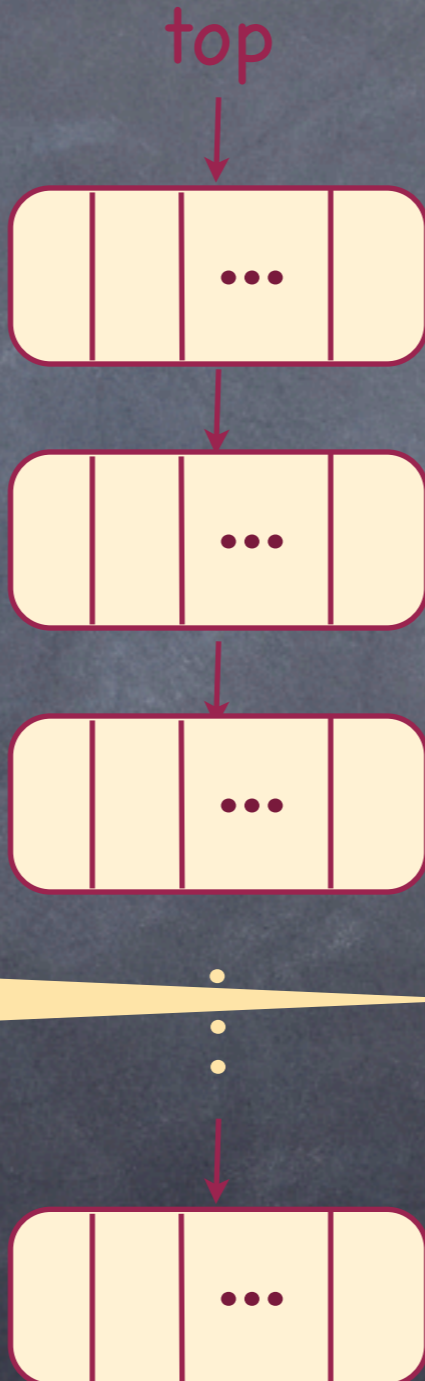
add/remove  
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4:   if successful:  
5:     return  
6:   else:  
7:     try to repair the stack  
8:     goto loop (retry)
```



# Restricted-out-of-order k-Stack

lock-free = non-blocking



k-segment

add/remove  
segment

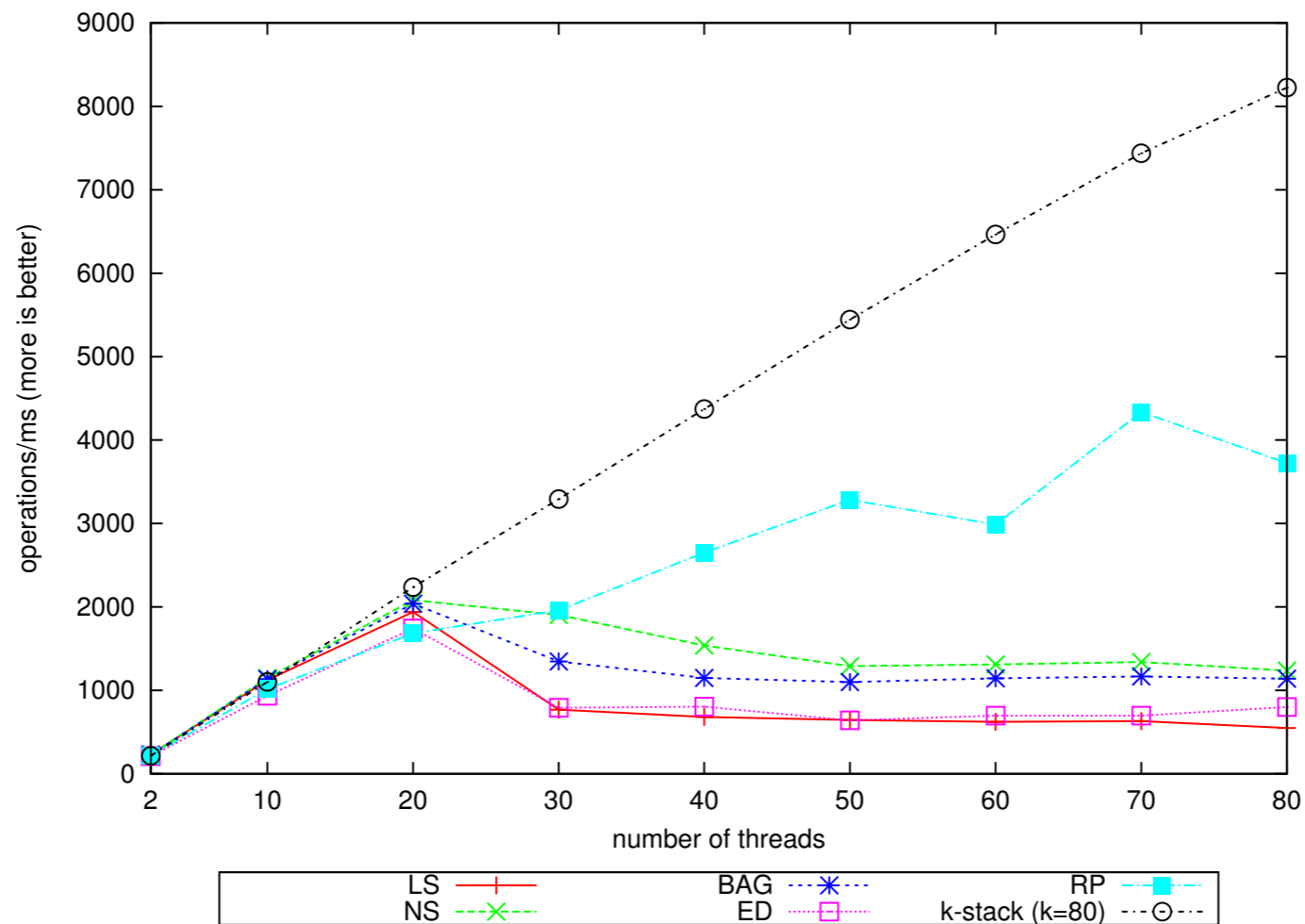
CAS - based

```
1: loop:  
2:   read consistent state  
3:   try to add/remove an item (*)  
4:   if successful:  
5:     return  
6:   else:  
7:     try to repair the stack  
8:     goto loop (retry)
```



# Stack

## Scalability comparison

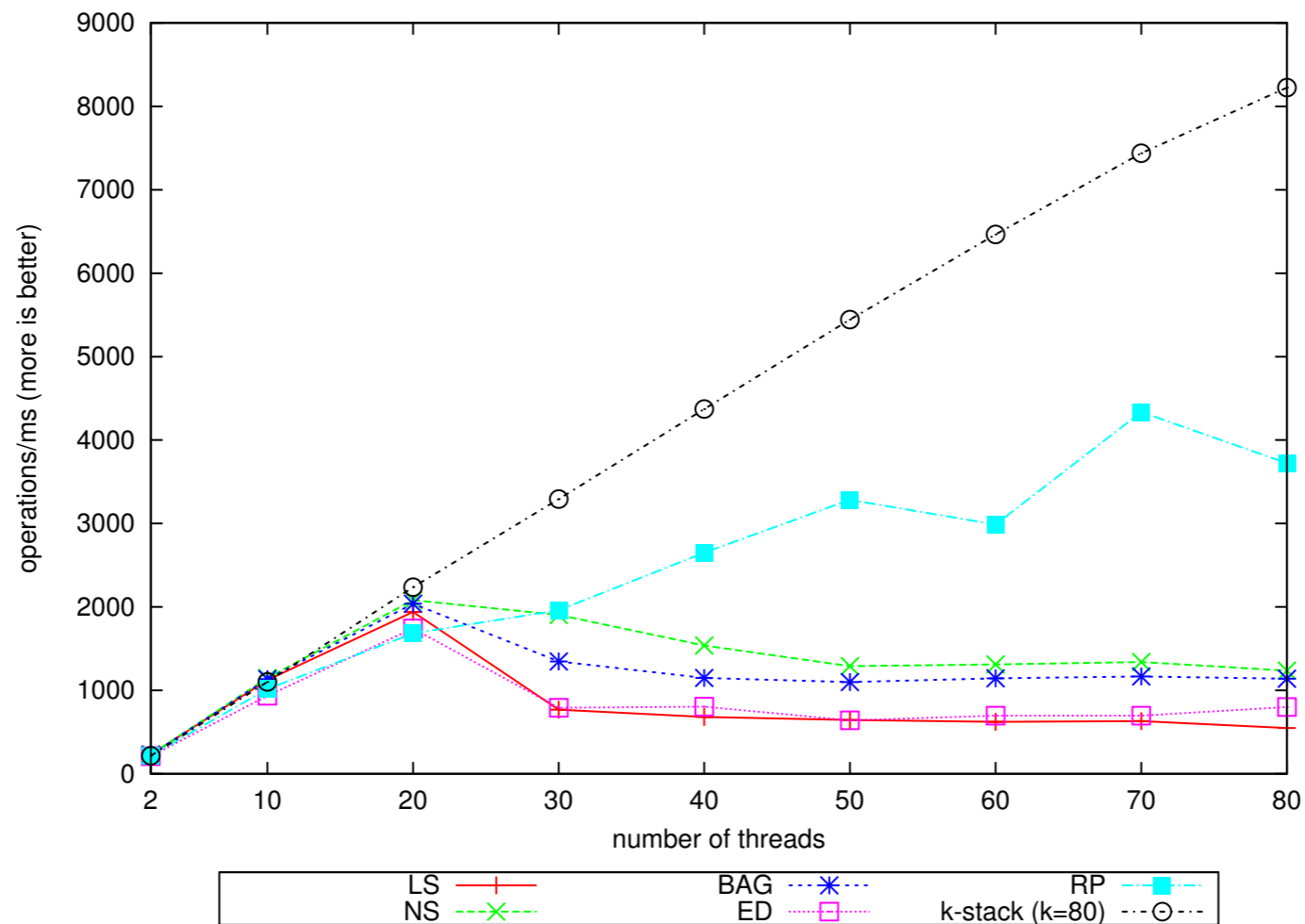




# Stack

## Scalability comparison

"80"-core machine

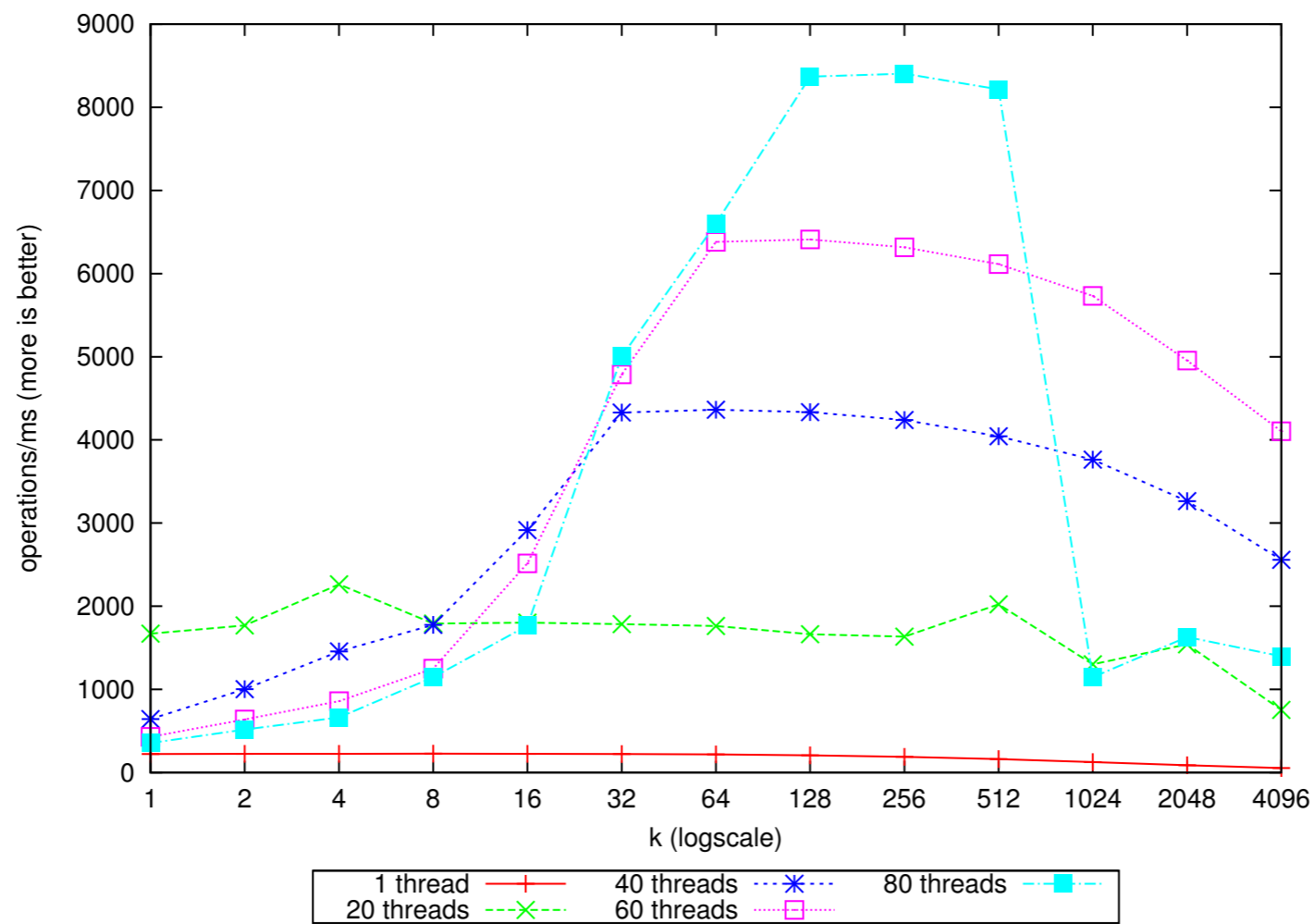




# k-Stack

The more relaxed, the better

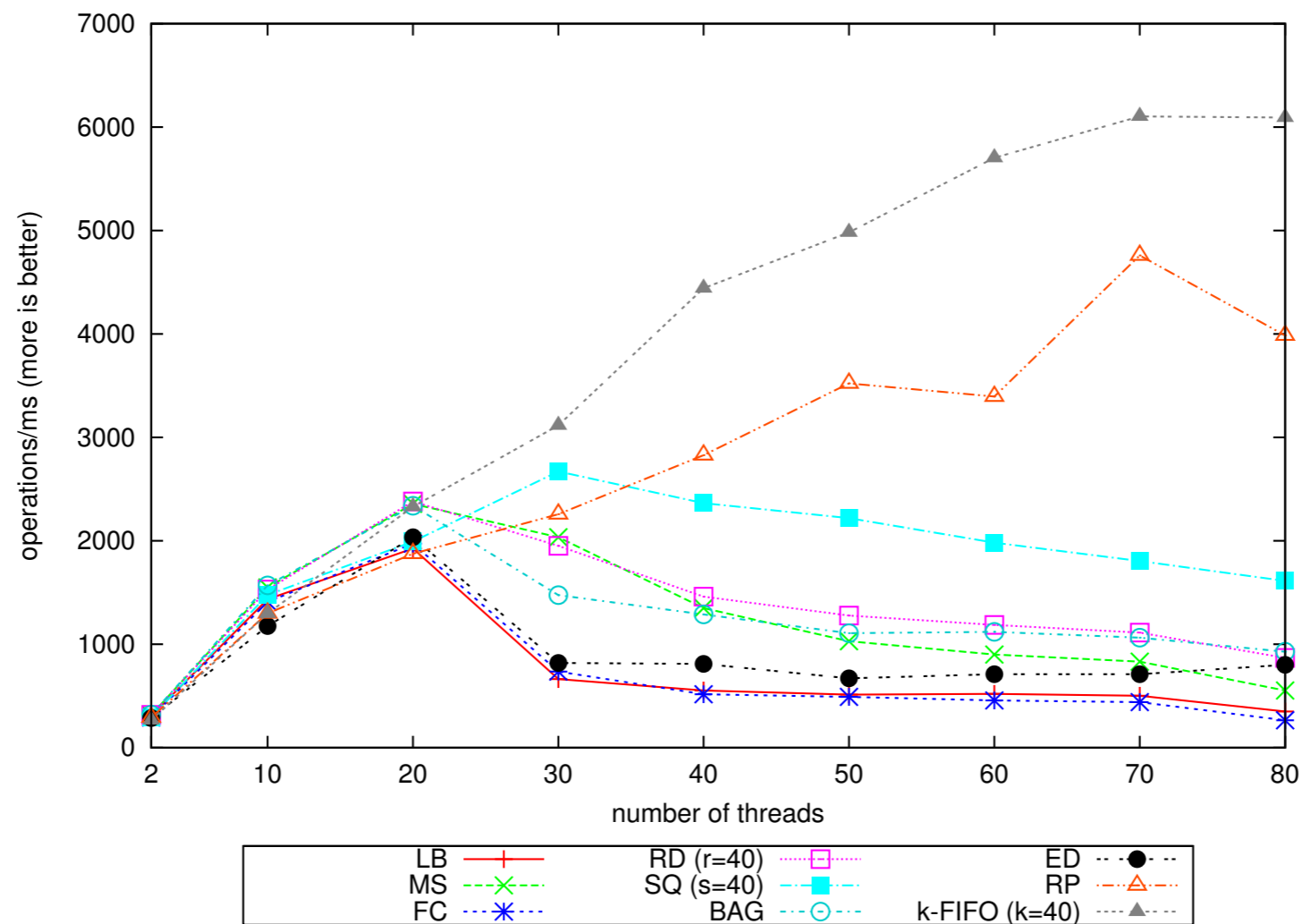
lock-free  
segment stack





# Queue

## Scalability comparison

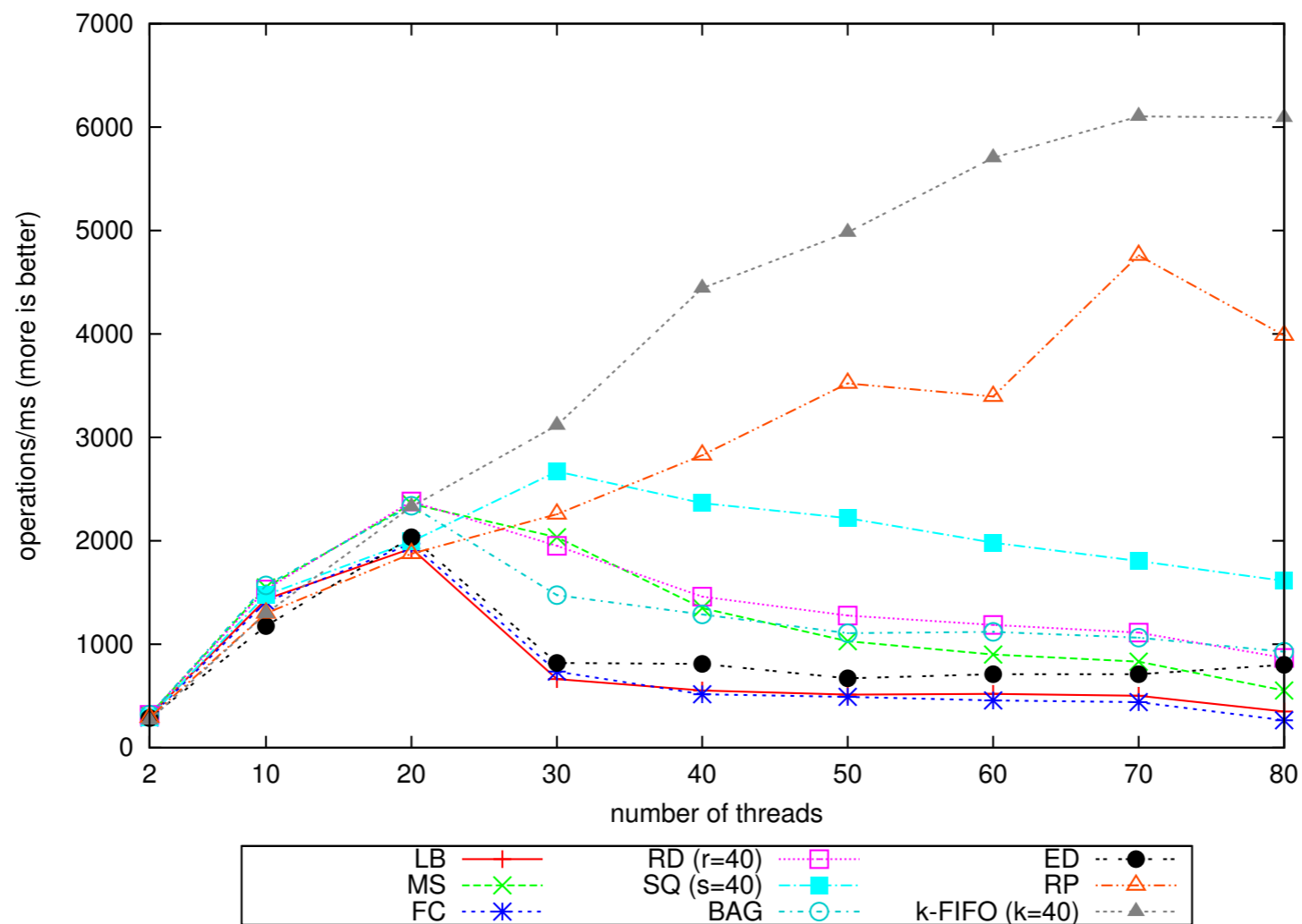




# Queue

## Scalability comparison

"80"-core machine

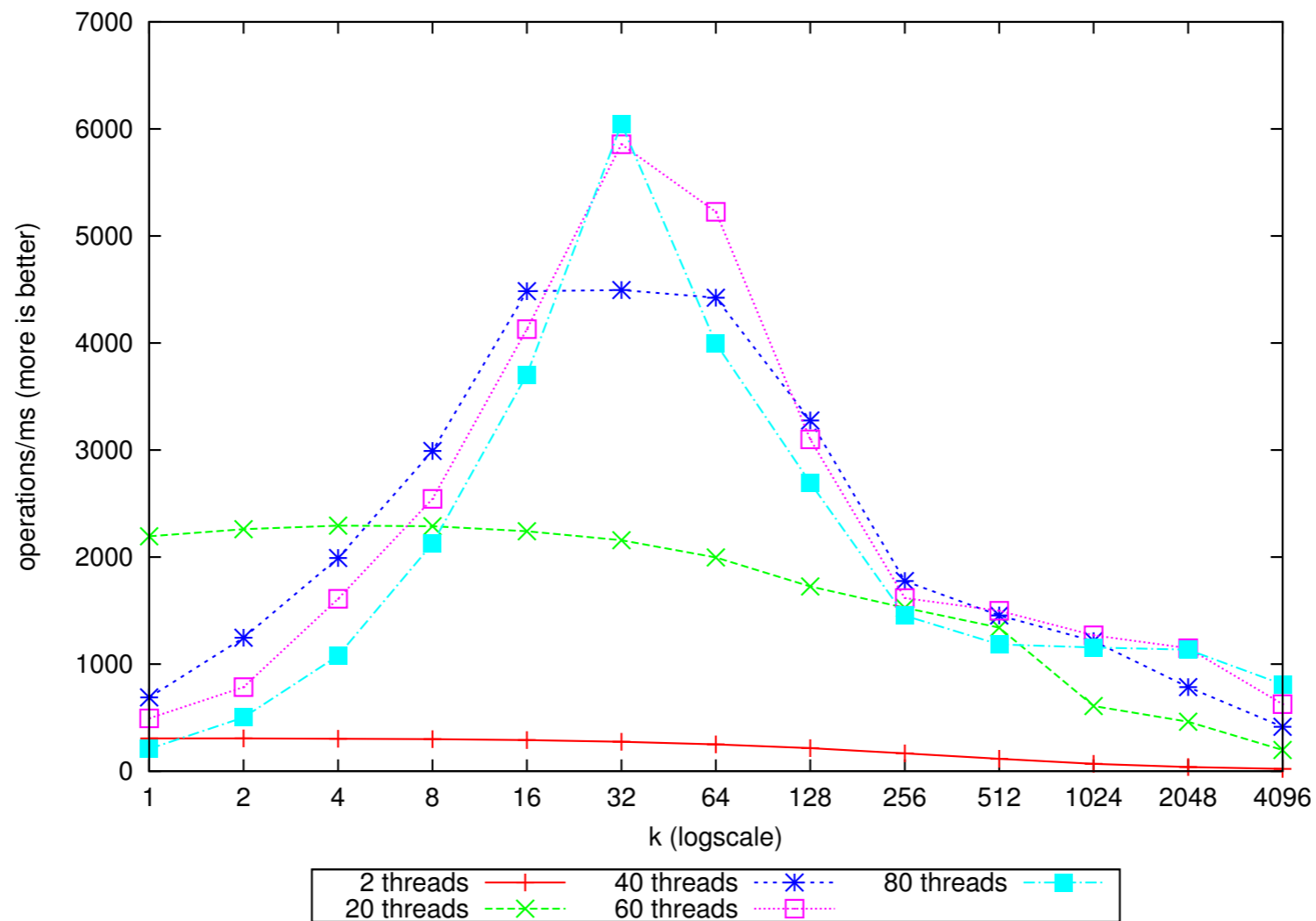




# k-Queue

The more relaxed, the better

lock-free  
segment queue





# Conclusions

## Contributions

Framework for quantitative relaxations  
generic relaxations, concrete examples,  
efficient implementations exist



# Conclusions

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## Difficult open problem

How to get from theory to practice?



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generic relaxations, concrete examples,  
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## Difficult open problem

How to get from theory to practice?

THANK YOU



# For the future

- Study applicability
- Learn from efficient implementations



# For the future

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which applications  
tolerate relaxation ?

maybe there is  
nothing to tolerate!

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towards synthesis

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THANK YOU